



Attorney Docket No. 5649-1060

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Moo-Sung Chae

Application Serial No.: 10/609/071

Allowed: December 7, 2004

Filed: June 27, 2003

Confirmation No.: 7501

For: INTEGRATED CIRCUIT MEMORY DEVICES INCLUDING ACTIVE LOAD
CIRCUITS AND RELATED METHODS

Date: February 9, 2005

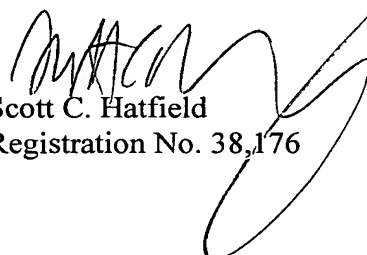
Mail Stop ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUBMISSION OF FORMAL DRAWINGS
WITH ISSUE FEE PAYMENT

Sir:

In response to the requirement for new drawings as set forth in Paper No. Mail Date 11/24/04 in the above application, enclosed is one set (4 sheets) of new formal drawings. It is requested that these new drawings be substituted for the originally filed informal drawings.

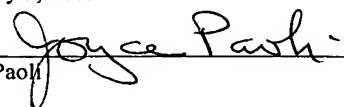
Respectfully submitted,


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CERTIFICATE OF MAILING UNDER 37 CFR 1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Mail Stop ISSUE FEE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on February 9, 2005.


Joyce Paoli